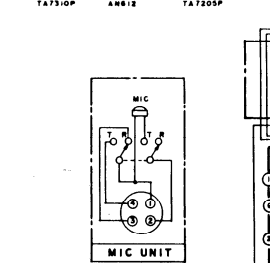
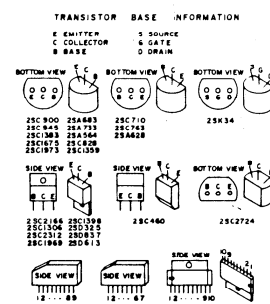
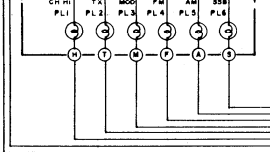
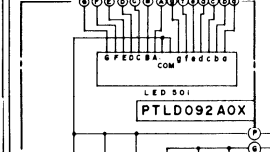
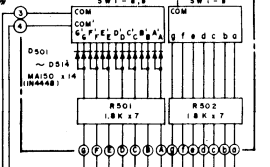
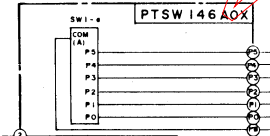
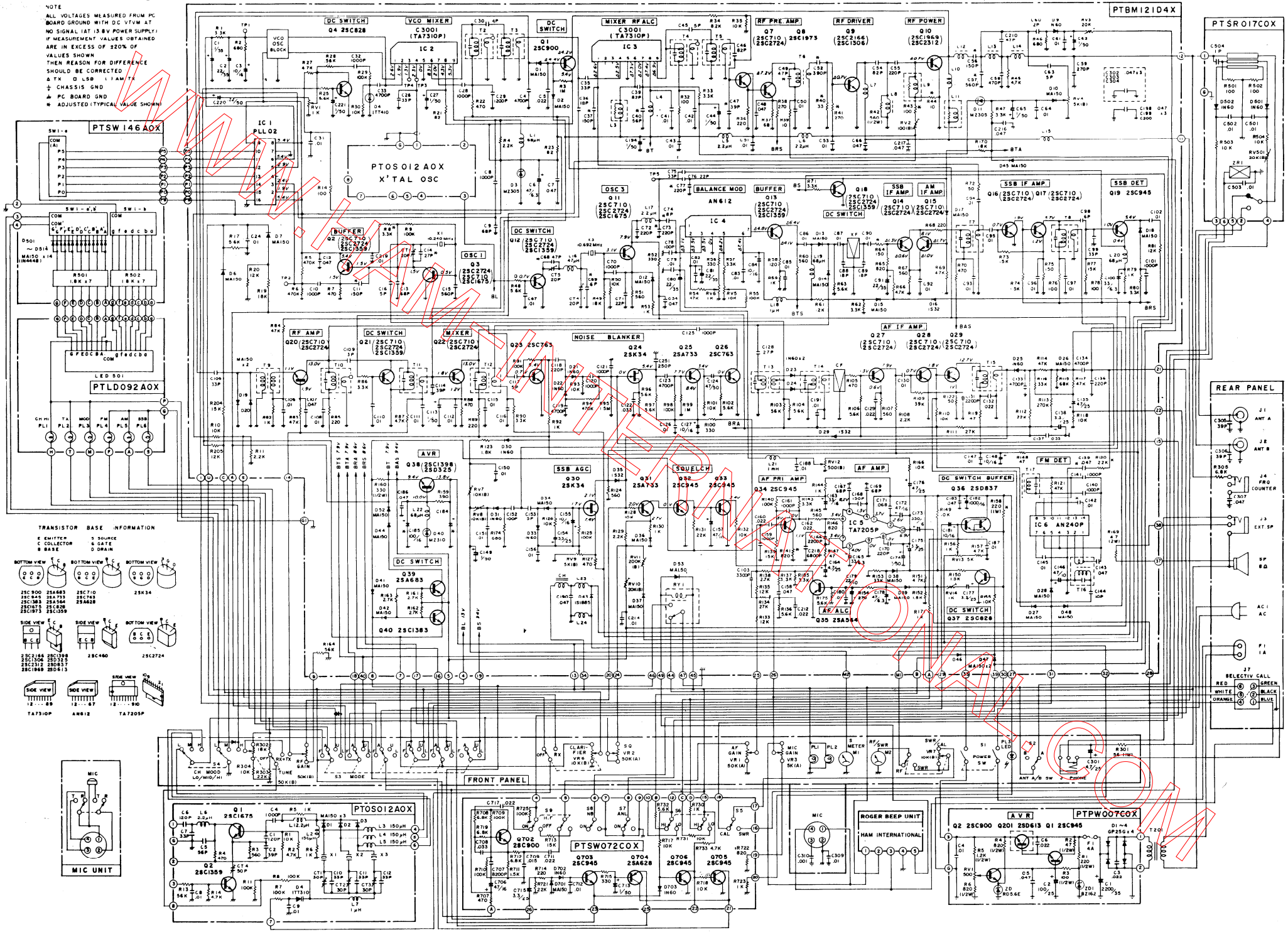


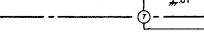
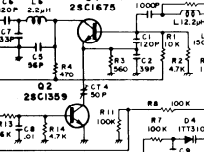
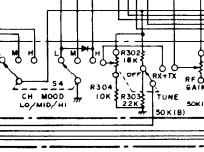
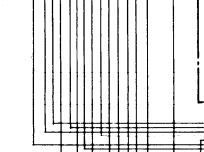
Schematic Diagram

NOTE
 ALL VOLTAGES MEASURED FROM PC BOARD GROUND WITH DC VVM AT NO SIGNAL. IAT 13.8V VVM SUPPLY IF MEASUREMENT VALUES OBTAINED ARE IN EXCESS OF 320% OF VALUES SHOWN THEN REASON FOR DIFFERENCE SHOULD BE CORRECTED
 * TX - Q L59 1.5 AM
 † CHASSIS GND
 ‡ PC BOARD GND
 * ADJUSTED (TYPICAL VALUE SHOWN)



TRANSISTOR BASE INFORMATION

B	SWITCH	SOURCE
C	COLLECTOR	GATE
E	BASE	DRAIN



PTOS012AOX