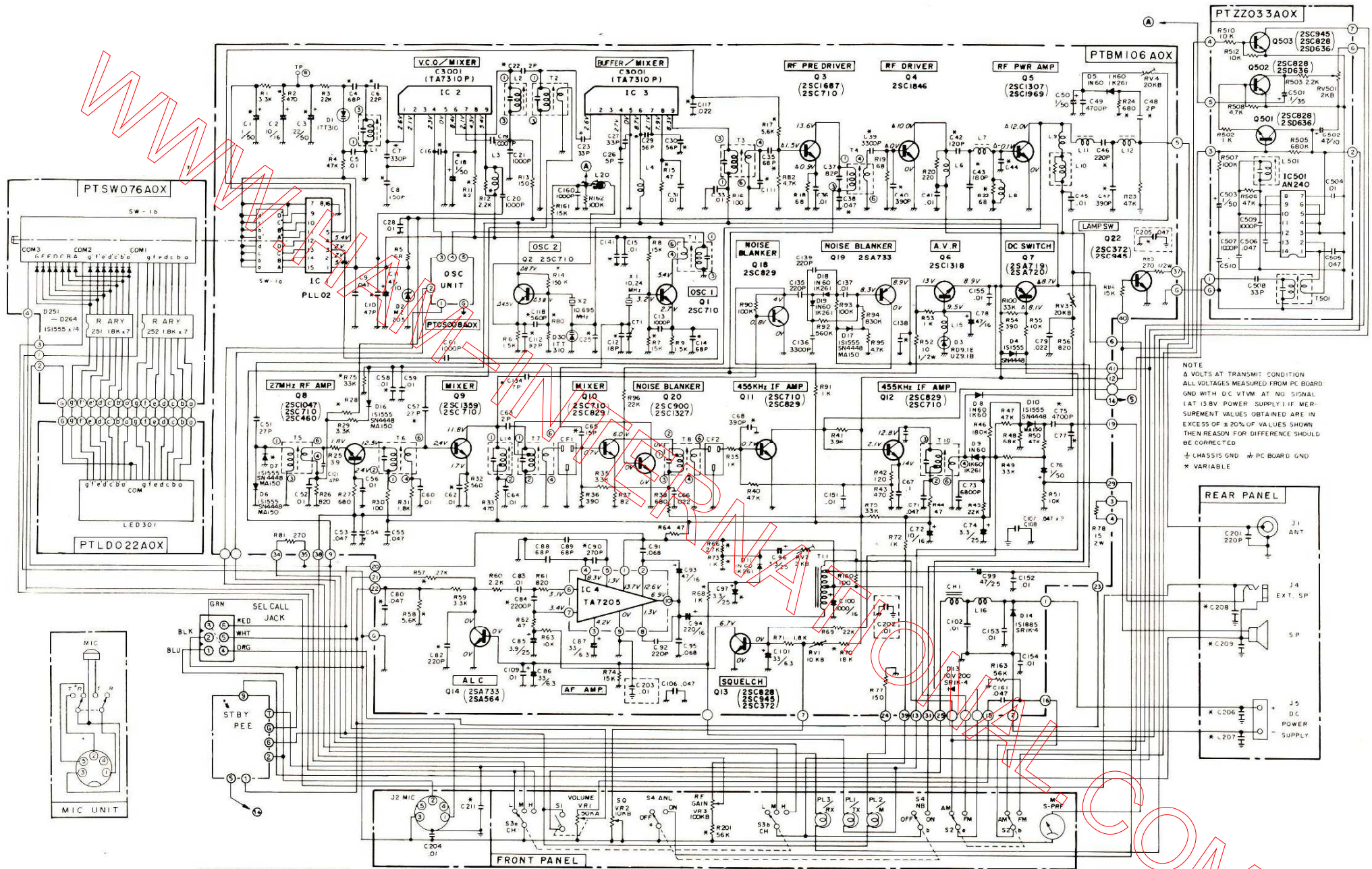
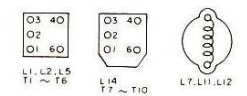


Schematic Diagram



NOTE
 Δ VOLTS AT TRANSMIT CONDITION
 ALL VOLTAGES MEASURED FROM PC BOARD
 GND WITH DC VTMN AT NO SIGNAL
 (AT 13.5V POWER SUPPLY) IF MEASUREMENT VALUES OBTAINED ARE IN EXCESS OF ±20% OF VALUES SHOWN THEN REASON FOR DIFFERENCE SHOULD BE CORRECTED
 † CHASSIS GND ‡ PC BOARD GND
 * VARIABLE

TRANSFORMER TERMINATION INFORMATION (BOTTOM VIEW)



TRANSISTOR BASE INFORMATION

