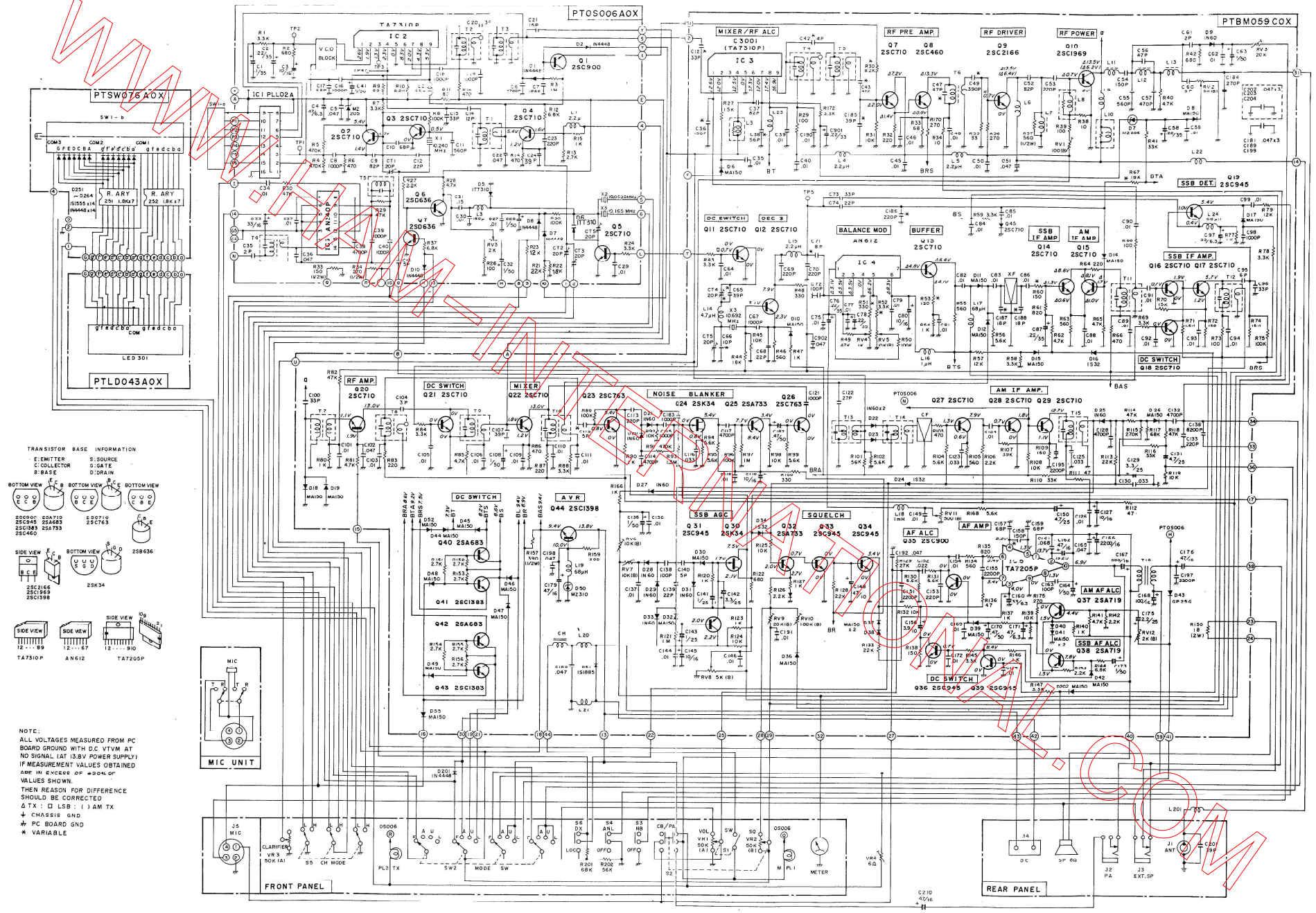
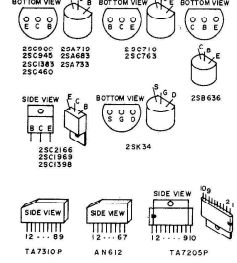


# Schematic Diagram



TRANSISTOR BASE INFORMATION

E: EMITTER S: SOURCE  
 C: COLLECTOR G: GATE  
 B: BASE D: DRAIN



NOTE:  
 ALL VOLTAGES MEASURED FROM PC BOARD GROUND WITH D.C. VTVM AT NO SIGNAL (AT 13.8V POWER SUPPLY)  
 IF MEASUREMENT VALUES OBTAINED ARE IN EXCESS OF ±30% OF VALUES SHOWN, THEN REASON FOR DIFFERENCE SHOULD BE CORRECTED  
 Δ TX : □ LSB : ( ) AM TX  
 + CHASSIS GND  
 \* PC BOARD GND  
 \* VARIABLE